

FIG.1

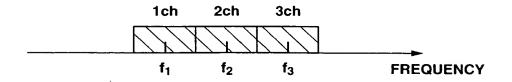


FIG.2

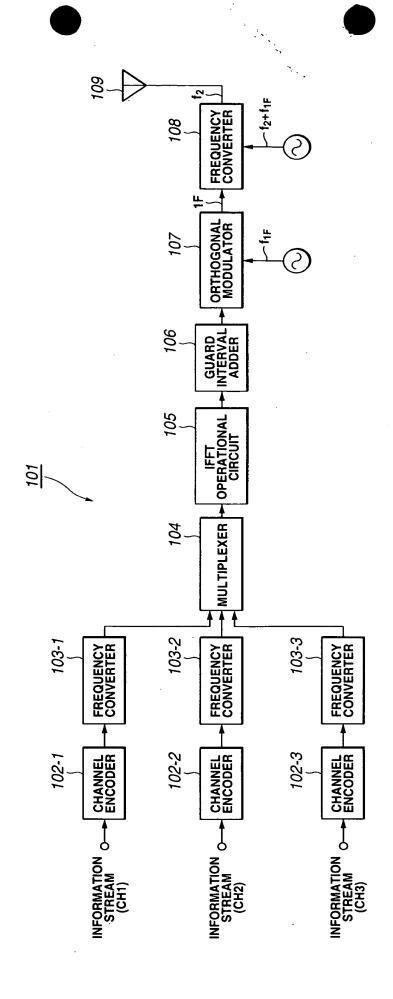


FIG.3

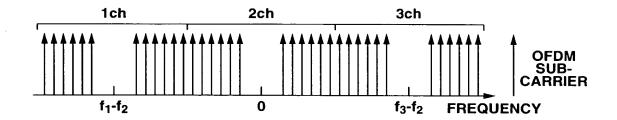


FIG.4

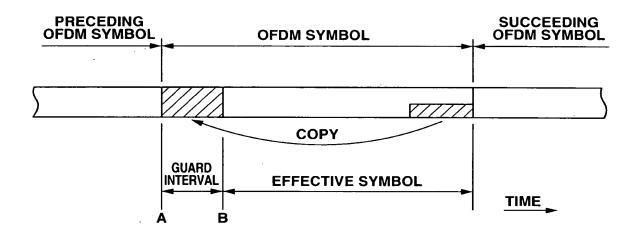


FIG.5

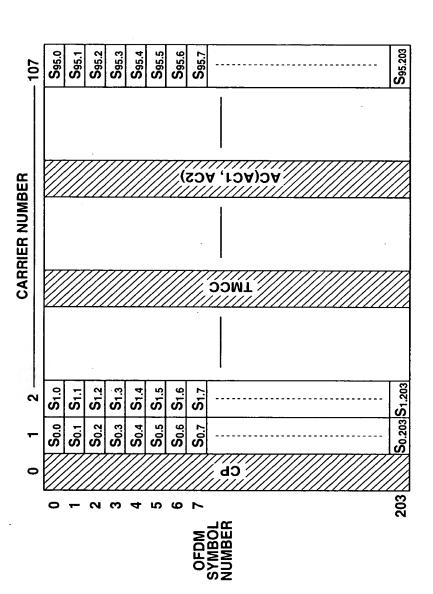


FIG.6

	107	S 95.0	S 95.1	. S95.2	S 95.3	S _{95.4}	S 95.5	S 95.6	S 95.7					 		S 95.201	S95.202	S 95.203
			 // 						/// CS	/// V	/// LOV	/// d)) ///	A ///					
			/// 	<u>///</u>						3 5	W.							
	12	SP	S11.1	S _{11.2}	S _{11.3}	SP												
CARRIER NUMBER	Ξ	S _{10.0}		\$10.2	S _{10.3}	S10.4												
	9	S _{9.0}	S _{9.1}	S _{9.2}	S _{9.3}	S _{9.4}										—	- 21	
ER N	တ	S _{8.0}			SP	S _{8.4}										1 S8.20	.202 S4.202 S5.202 SP S8.202 S7.202 S8.202	.203 S4.203 S5.203 S6.203 S7.203 S8.203 SF
SARRI	œ	S _{7.0}	I S7.1	\$ 57.2	S8.3	S7.4										N S7.20	57.20	358.20
ပ	7	0.98	1 S6.1	Se.2	3 \$7.3	4 S6.4										D1 S8.20	S8.20	03 <mark>S</mark> 7.20
	9	0 \$5.0	1 S _{5.1}		3 S _{6.3}	4 S _{5.4}										01 \$5.2	02 SP	03 S6.2
		0 84.0	1 S4.1	2 S _{5.2}	3 \$5.3	4 S4.4										01 84.2	02 55.2	03 \$5.2
	4	.0 \$3.0	S S3.1	.2 \$4.2	.3 S 4.3	.4 S _{3.4}										5//S3.2	02 54.2	03 84.2
	3		1 SP			.4 S2	SP									188 LOG	202 54.2	203 \$5.2
	- 2	0.0 S1.0	1.1 \$2.1	S _{1.2} S _{2.2}	1.3 \$2.3	S1.4 S1.4										201 \$2.2	202 52.2	203 52.2
	C	.6 S _{0.0}	S _{0.1} S _{1.1}	S _{0.2} S ₁	S _{0.3} S _{1.3}	SP S _{0.4}									Sp	S0.201 S1.201 S2.201 S9.201 S4.201 S5.201 S8.201 S7.201 S8.201	So.202 S1.202 S2.202 S4	203 \$1.
		0	-	2 S(လ	4 S								 	200	201 S ₀ .	202 So.	203 So.203 S1.203 S2.203 S5
						-				OFDM	NUMBER				CA	-	CA	CA.

FIG.7

SEGMENT NO.	
СР	0
AC1_1	35
AC1_2	79
AC2_1	3
AC2_2	72
AC2_3	85
AC2_4	89
TMCC 1	49
TMCC 2	61
TMCC 3	96
TMCC 4	99
TMCC 5	104

FIG.8

CARRIER ARRANGEMENT OF TMCC AND AC OF SYNCHRONOUS MODULATOR

SEGMENT NO.	
AC1_1	35
AC1_2	79
TMCC 1	49

FIG.9

B ₀	REFERENCE FOR DIFFERENTIAL DEMODULATION
B ₀ ~ B ₁₆	SYNCHRONIZING SIGNAL (W0=0011010111101110, W1=1100101000010001)
B ₁₇ ~ B ₁₉	IDENTIFICATION OF SEGMENT FORMAT (DIFFERENTIAL 111, SYNCHRONOUS 000)
B ₂₀ ~ B ₁₂₁	TMCC INFORMATION (102 BITS)
B ₁₂₂ ~ B ₂₀₃	PARITY BITS

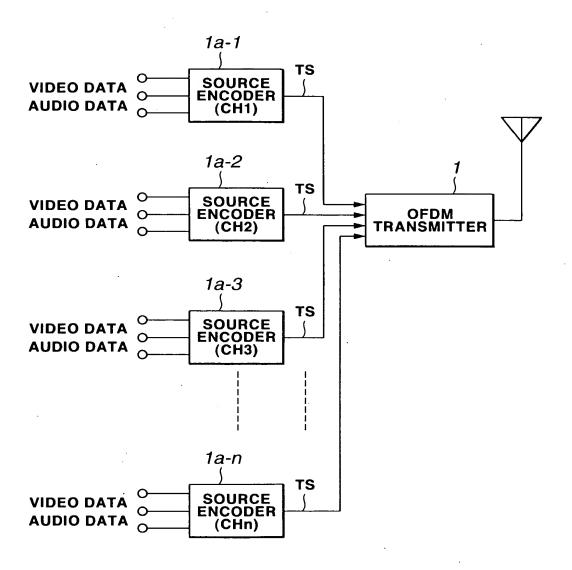


FIG.11

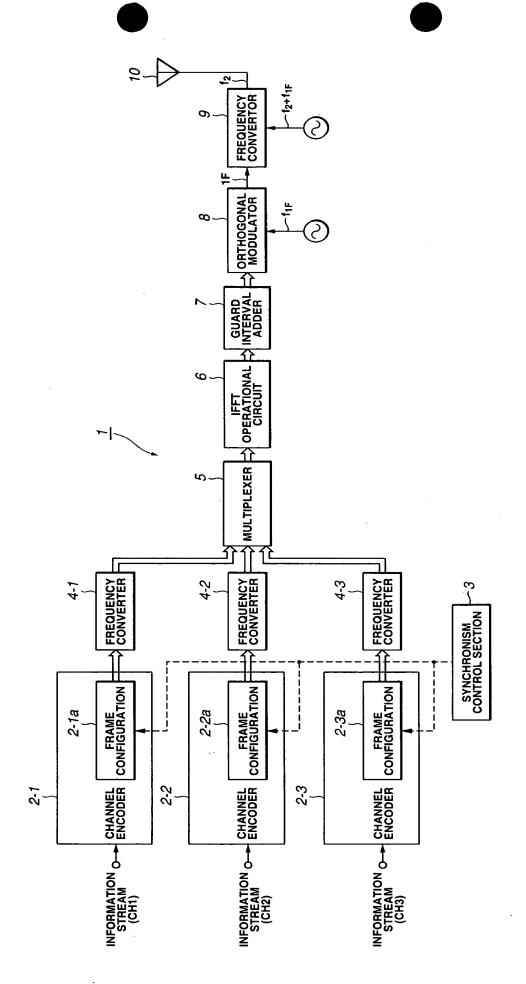


FIG. 12

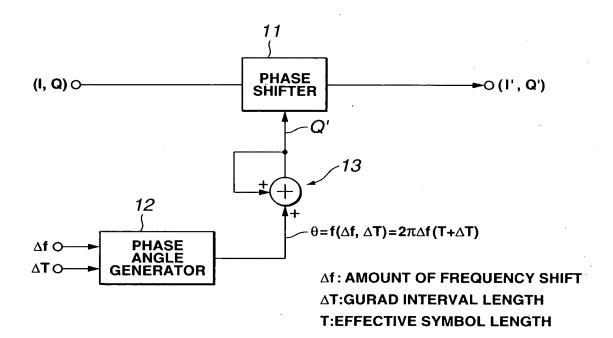


FIG.13

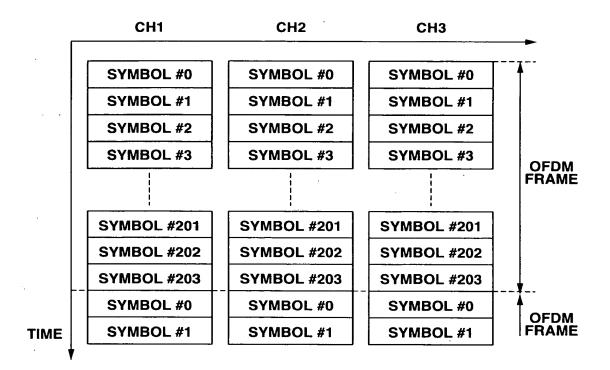


FIG.14

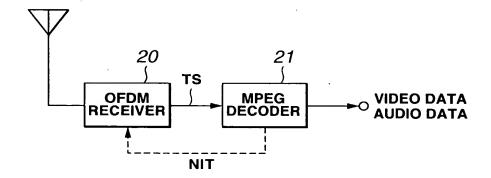
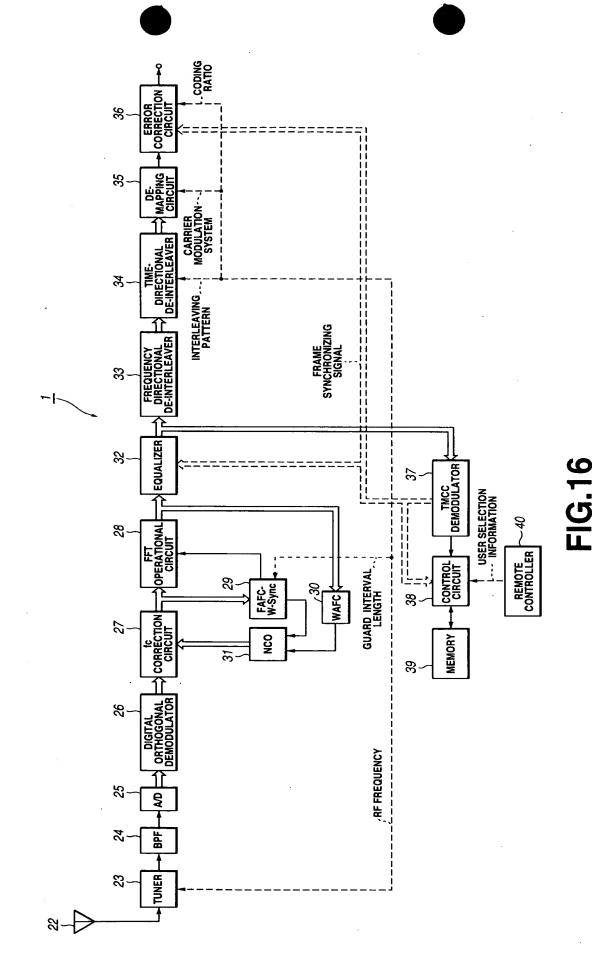


FIG.15



BIT ASSIGNMENT	EXPLANATION
B ₁₁₀ ~ B ₁₁₃	NUMBER OF CONNECTED SEGMENTS
B ₁₁₄ ~ B ₁₁₇	SEGMENT NO. OF SIGNAL TO BE TRANSMITTED

VALUE (b ₁₁₃ , b ₁₁₂ , b ₁₁₁ , b ₁₁₀)	MEANING
0000	RESERVED
0001	RESERVED
0010	2 SEGMENTS
0011	3 SEGMENTS
0100	4 SEGMENTS
•	•
1100	12 SEGMENTS
1101	13 SEGMENTS
1110	RESERVED
1111	INDEPENDENT TRANSMISSION

FIG.18

•		1
SEGMENT	#12	
SEGMENT	#10	
SEGMENT	8	
SEGMENT SEGMENT SEGMENT SEGMENT SEGMENT SEGMENT SEGMENT SEGMENT	9#	
SEGMENT	#	
SEGMENT	#5	
SEGMENT	0#	
SEGMENT	#	
SEGMENT	۳ #	
SEGMENT	\$ #	
SEGMENT	2 #	
SEGMENT	6#	
SEGMENT	#11	

FIG. 19

·	SEGMENT	SEGMENT	SEGMENT
	#1	#0	#2

SEGMENT	SEGMENT	SEGMENT	SEGMENT	SEGMENT	SEGMENT
#5	#3	#1	#0	#2	#4

VALUE (b ₁₁₇ , b ₁₁₆ , b ₁₁₅ , b ₁₁₄)	MEANING
1111	SEGMENT #0
1110	SEGMENT #1
1101	SEGMENT #2
0011	SEGMENT #12
0010	RESERVED
0001	RESERVED
0000	RESERVED

FIG.22

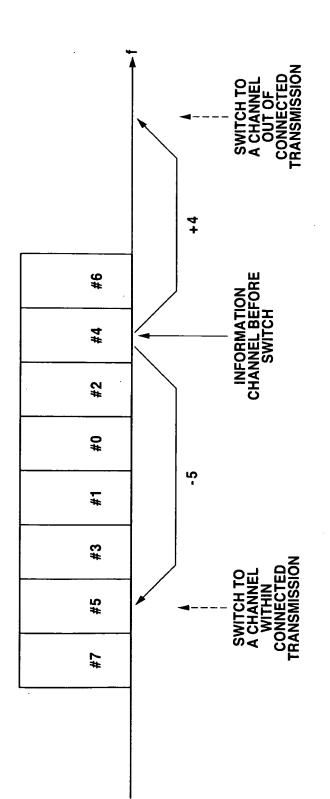


FIG.23

000	CONNECTED TRANSMISSION GROUP #0
001	CONNECTED TRANSMISSION GROUP #1
010	CONNECTED TRANSMISSION GROUP #2
011	CONNECTED TRANSMISSION GROUP #3
100	CONNECTED TRANSMISSION GROUP #4
101	CONNECTED TRANSMISSION GROUP #5
110	CONNECTED TRANSMISSION GROUP #6
111	INDEPENDENT TRANSMISSION

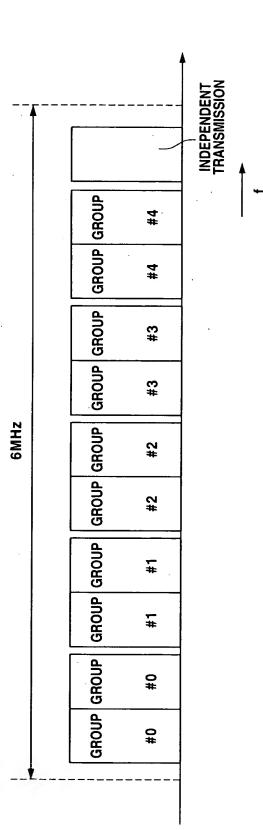


FIG.25